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To:	Examiner Paul W. Schlie	From:	Timothy N. Trop
Company:	US Patent Office	Date:	April 11, 2006
Fax:	(571) 273-8300	Pages:	8 (including cover sheet)
Your Re:	10/717,085	Our Re:	ITL 1059US

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	Ramesh V. Peri et al.	§	Art Unit:	2186
Serial No.:	10/717,085	§	Examiner:	Paul W. Schlie
Filed:	November 19, 2003	§	Docket:	ITL1059US P17918
For:	Accessing Data From Different Memory Locations in the Same Cycle	§	Assignee:	Intel Corporation

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed March 16, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: April 11, 2006
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the date indicated above.
Cynthia L. Hayden
Cynthia L. Hayden